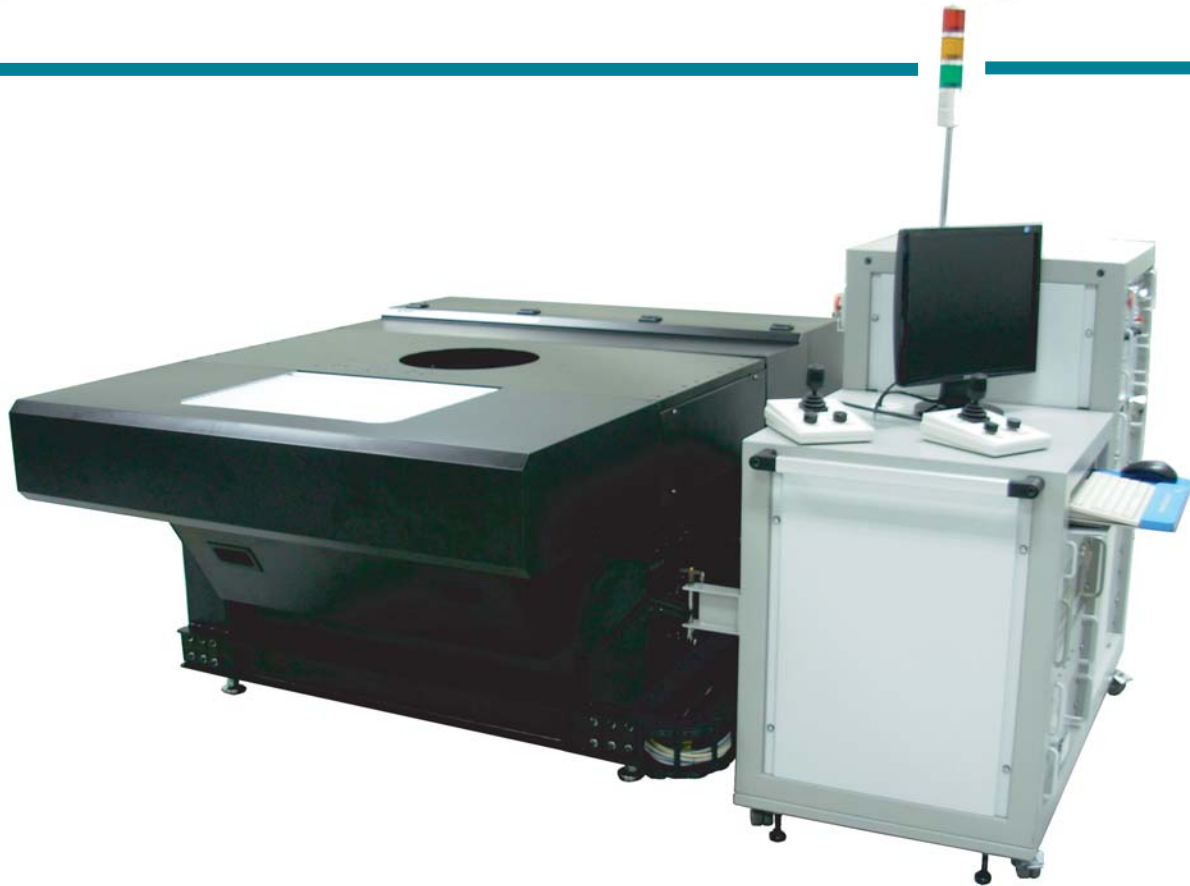


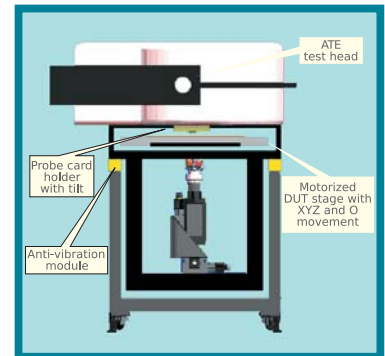
# SEMICAPS 5000 CONFIGURATION

## TESTER-DOCKED WAFER PROBER SYSTEM

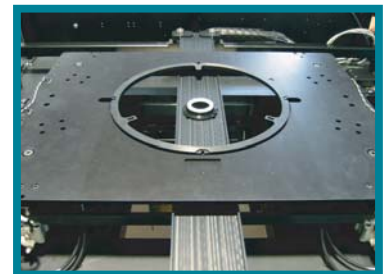


## Features

- ATE direct docking using Production Probe Cards
- Patent pending wafer support allows RSIL imaging while docked to ATE
- Probe landing using optical registration
- High resolution stage with 20nm resolution and 0.5 $\mu$ m repeatability
- Centric and Aplanatic Refractive Solid Immersion Lens (RSIL) option
- CAD interface option
- Fully automatic die-to-die techniques include:
  - Laser Timing Probe (LTP) with frequency mapping for locating signals
  - Scanning Optical Microscopy (SOM) with best-in-class sensitivity for static and dynamic laser induced techniques
  - Photon Emission Microscopy (PEM) with LN<sub>2</sub>-free, low noise InGaAs or SI-CCD camera
  - Thermal Microscopy (THM) with InSb camera



ATE-Docked



Wafer Prober